



2814
#7/Election
6-17-03
C. Moore

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Fernando Gonzalez et al.

Serial No.: 10/081,915

Filed: February 22, 2002

For: METHOD FOR FORMING
CONDUCTORS IN
SEMICONDUCTOR
DEVICES

§ Group Art Unit: 2814

§ Examiner: Perlata, G.

§ Atty. Docket: MCRO:1254/FLE
§ 94-0281.04

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

CERTIFICATE OF MAILING
37 C.F.R. 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria VA, on the date below:

June 9, 2003

Date


Michael G. Fletcher

Sir:

RESPONSE TO ELECTION REQUIREMENT

In Response to the Official Action mailed on May 7, 2003, Applicants respectfully request reconsideration of the above-identified application in view of the remarks set forth below.

REMARKS

In the Official Action, the Examiner required Applicants to elect between species A, in which a method for making a memory device includes forming a plurality of diode access device memory cells, and a species B, in which a method for making a memory device includes forming a plurality of memory cells, each memory cell comprising an element programmable to multiple states of resistance. Although Applicants do necessarily agree with the identification of species set forth by the Examiner, Applicants have

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